

PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: John Bruno  
Serial No.: 10/064,856  
Filing Date: August 23, 2002  
Confirmation No.: 4595

Examiner: Nathan W. Ha  
Art Unit: 2814  
Our File No.: 00100.02.0038  
Docket No.: 00100.02.0038

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Title: **INTEGRATED CIRCUIT HAVING MEMORY DISPOSED THEREON AND  
METHOD OF MAKING THEREOF**

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

*Certificate of Facsimile*  
I hereby certify that this paper is being sent to the United States  
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22313-1450, on this date.

8/25/04

Date

Therese Anagnos

**RESPONSE TO FINAL OFFICE ACTION**

Dear Sir:

In response to the Final Office Action mailed June 25, 2004, Applicant submits the following amendment and response.

Status of the Claims begin on page 2 of this paper.

Remarks begin on page 6 of this paper.